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CONFIRMATION NO. 58

Bib Data Sheet

SERIAL NUMBER 10/685,872	FILING DATE 10/15/2003 RULE	CLASS 257	GROUP ART UNIT 2822	ATTORNEY DOCKE NO. MEG02-015
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APPLICANTS

Mou-Shiung Lin, Hsin-Chu City, TAIWAN;

** CONTINUING DATA *****

This application is a CIP of 10/278,106 10/22/2002 PAT 6,734,563 which is a DIV of 09/691,497 10/18/2000 PAT 6,495,442

W

** FOREIGN APPLICATIONS *****

W none

IF REQUIRED, FOREIGN FILING LICENSE GRANTED
** 01/22/2004

**** SMALL ENTITY**

Foreign Priority claimed	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR	LEETS	OTAL	INDEPEND
35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	COUNTRY	DRAWING	LAIMS	CLAIMS
Verified and Acknowledged	Examiner's Signature 	TAIWAN	9	100	11
	Initials 				

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INTRODUCTION

Post passivation interconnection schemes on top of the IC chips

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